3 bit full adder

**Truth table:**

a b c x y

(s) (C)

0 0 0 0 0

0 0 1 1 0

0 1 0 1 0

0 1 1 0 1

1 0 0 1 0

1 0 1 0 1

1 1 0 0 1

1 1 1 1 1

**Output:**

**Diagram, schematic

Description automatically generated**